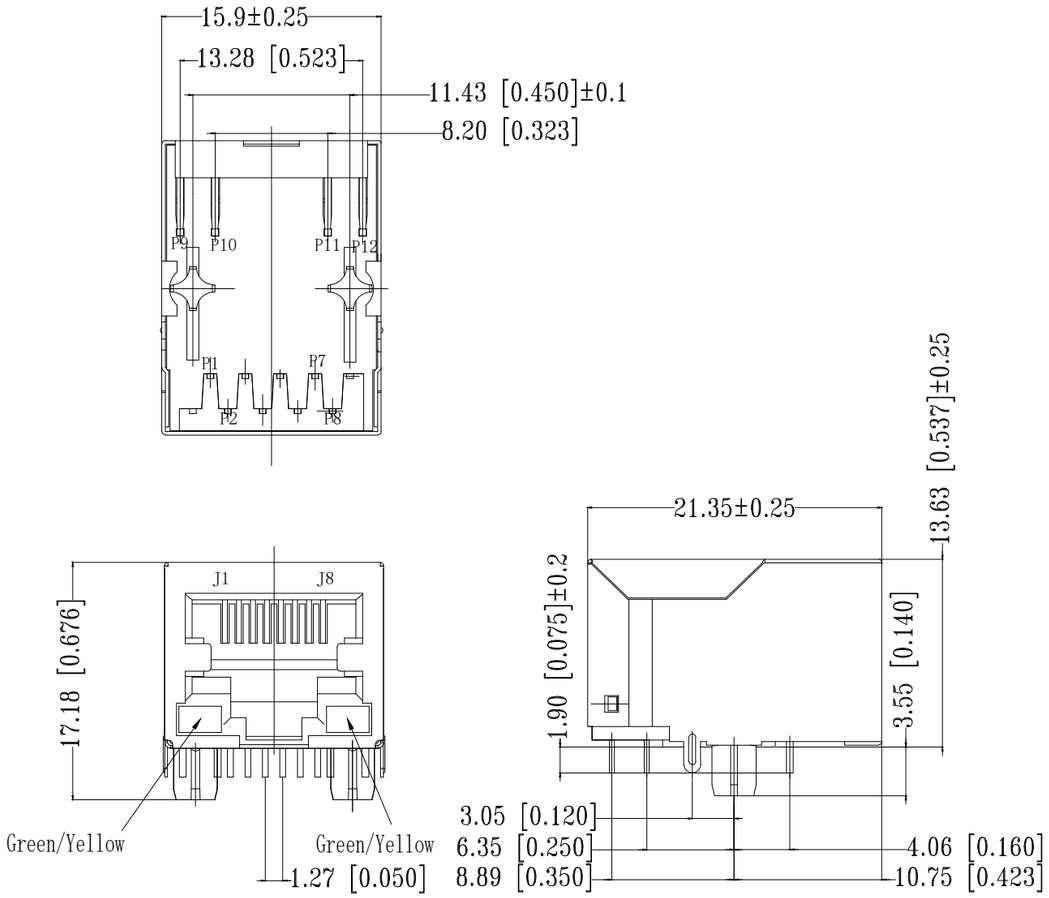
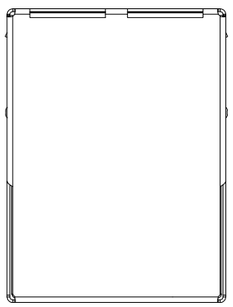
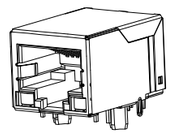
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REVISIONS					
P	LTR	DESCRIPTION	DATE	DWN	APVD
	A1	NEW RELEASE	11JUN2018	GCM	MSZ





1. CONNECTOR MATERIAL:
   HOUSING: LCP BLACK UL94 V-0
   INSERT: LCP BLACK UL94 V-0
   SHIELD: BRASS
   SHIELD PLATING: NICKEL
   CONTACT: COPPER ALLOY
   CONTACT PLATING: SELECTIVE GOLD, MIN. 0.76µm (30µinch) IN CONTACT AREA OVER MIN. 1.27µm (50µinch) NICKEL
   SOLDER PIN PLATING: 3.05µm (120µinch) TIN OVER 1.02µm (40µinch) NICKEL OVER ALL
   SHIELDING PIN PLATING: NICKEL
2. PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED SEE ELECTRICAL CIRCUIT DIAGRAM FOR OMITTED PINS
3. RJ45 CAVITIES CONFORM TO FCC RULES AND REGULATION PART 68
4. THE PART IS RECOMMENDED FOR REFLOW SOLDERING PROCESS PEAK SOLDERING: TEMPERATURE MAX. +260° C, MAX. 10s
5. OPERATING TEMPERATURE: T= -40° C TO +85° C
6. STORAGE TEMPERATURE: T= -40° C TO +85° C
7. UNLESS OTHERWISE SPECIFIED, SEE TABLE FOR ALL DIMENSIONS TOLERANCES
8. JACK CONFIGURATION: 1 x 1
   TAB DIRECTION: DOWN
9. PACKAGING: REEL (T+R) ACCORDING TO PACKAGING SPECIFICATION 107-18116



1:1

7 TABLE OF TOLERANCE (mm)

RANGE	TOLERANCE
0–10	±0.15
>10–40	±0.25
>40–70	±0.30
>70	±0.40

THIS DRAWING IS A CONTROLLED DOCUMENT.

DIMENSIONS: mm [INCHES]

TOLERANCES UNLESS OTHERWISE SPECIFIED:
0 PLC ± –
1 PLC ± –
2 PLC ± –
3 PLC ± –
4 PLC ± –
ANGLES ± –
7

MATERIAL 1

FINISH 1

DWN	GANESH C M 11JUN2018
CHK	FRANZ MUELLER 11JUN2018
APVD	MARTIN SZELAG 11JUN2018
PRODUCT SPEC	108-94552
APPLICATION SPEC	114-94447
WEIGHT	

TE Connectivity

NAME: RJ45 JACK INT.MAG. 10/100 LED 1x1 – –

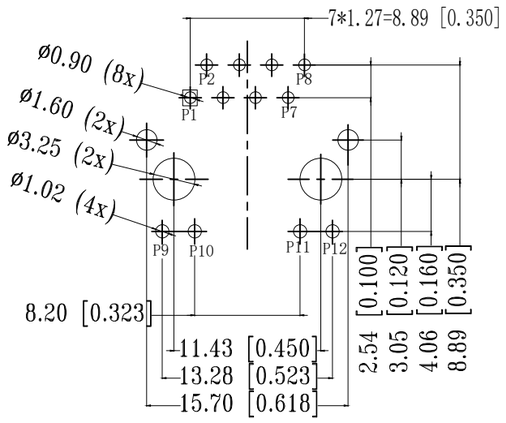
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A3	00779	C-2-2301994-6	–

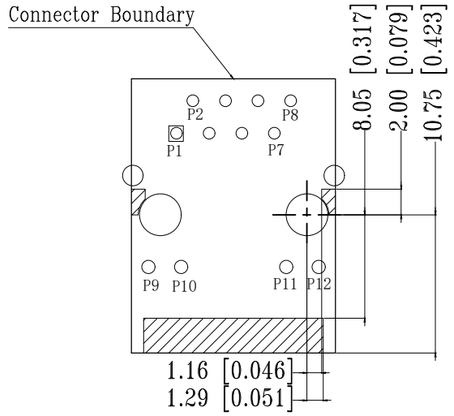
CUSTOMER DRAWING | SCALE 3:1 | SHEET 1 OF 3 | REV A1

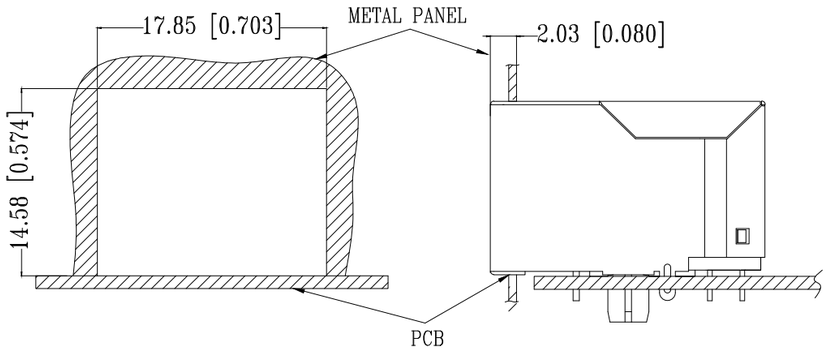
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REVISIONS					
P	LTR	DESCRIPTION	DATE	DWN	APVD
	–	SEE SHEET 1	–	—	—

SUGGESTED PCB LAYOUT







UNIT: mm / inch
TOLERANCES: ±0.10 / 0.004

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DIMENSIONS: mm [INCHES]

TOLERANCES UNLESS OTHERWISE SPECIFIED:
0 PLC ± –
1 PLC ± –
2 PLC ± –
3 PLC ± –
4 PLC ± –
ANGLES ± –
7

MATERIAL 1

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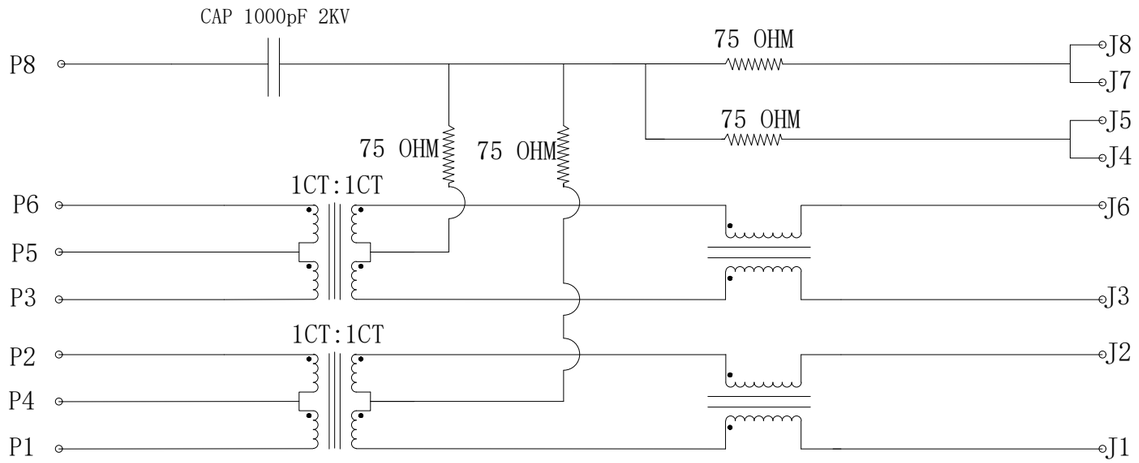
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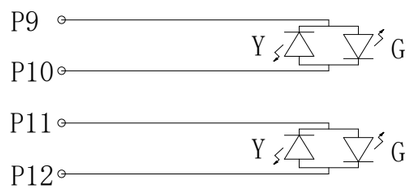
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REVISIONS

P	LTR	DESCRIPTION	DATE	DWN	APVD
	–	SEE SHEET 1	–	—	—

ELECTRICAL CIRCUIT DIAGRAM





Pin	Yellow	Green	Pin	Yellow	Green
P9	–	+	P11	–	+
P10	+	–	P12	+	–

1.0 Turn Ratio @100KHz: (P1~P2):(J1~J2) = 1:1±2%
(P3~P6):(J3~J6) = 1:1±2%

2.0 Primary Inductance: 350uH MIN @100KHZ,0.1V 8mA DC BIAS

3.0 DC Resistance: 1.2 OHMS MAX

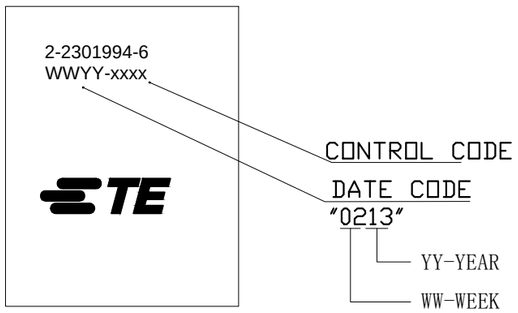
4.0 Insertion Loss: 1–100MHz –1.2dB MAX

5.0 Return Loss: 1–30MHz –16dB MIN
30–60MHz –12dB MIN
60–80MHz –10dB MIN

6.0 CROSS TALK: 1–100MHz –30dB MIN

7.0 COMMON TO COMMON MODE ATTENUATION: 1–100MHz –30dB MIN

8.0 Isolation: PHY Side to Line Side: 1500VAC or 2250VDC



FCC

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DIMENSIONS: mm [INCHES]

TOLERANCES UNLESS OTHERWISE SPECIFIED:
0 PLC ± –
1 PLC ± –
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4 PLC ± –
ANGLES ± –
7

MATERIAL 1

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NAME RJ45 JACK INT.MAG. 10/100 LED 1x1

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A3	00779	C-2-2301994-6	—

CUSTOMER DRAWING | SCALE 3:1 | SHEET 3 OF 3 | REV A1