

FRED Pt® Gen 5 Hyperfast Rectifier Diode, 1200 V, 60 A



SOT-227



**RoHS
COMPLIANT**

FEATURES

- Hyperfast and optimized Q_{rr}
- Best in class forward voltage drop and switching losses trade off
- Optimized for high speed operation
- 175 °C maximum operating junction temperature
- Electrically isolated base plate
- Large creepage distance between terminal
- Simplified mechanical designs, rapid assembly
- Designed and qualified for industrial level
- UL approved file E78996 
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

Featuring a unique combination of low conduction and switching losses, the VS-U5FX60FA120 is the right choice for high frequency converters, both soft switched / resonant. The semiconductor in the SOT-227 package is isolated from the copper base plate, allowing for common heatsinks and compact assemblies to be built.

These modules are specifically designed to improve efficiency of PFC and output rectification stages of EV / HEV battery charging stations, booster stage of solar inverters, and UPS applications, these devices are perfectly matched to operate with MOSFETs or high speed IGBTs.

PRIMARY CHARACTERISTICS

V_R	1200 V
V_F (typical) at 30 A, per diode	2.91 V
t_{rr} (typical) at 30 A, per diode	41 ns
$I_{F(DC)}$ per module at $T_C = 85\text{ °C}$	60 A
Type	Modules - diode, FRED Pt®
Package	SOT-227
Circuit configuration	Two separate diodes, parallel pin-out

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Cathode to anode voltage	V_R		1200	V
Continuous forward current per diode	I_F	$T_C = 85\text{ °C}$	30	A
Single pulse forward current per diode	I_{FSM}	$T_J = 25\text{ °C}$	150	
Maximum power dissipation per module	P_D	$T_C = 85\text{ °C}$	164	W
RMS isolation voltage	V_{ISOL}	Any terminal to case, $t = 1\text{ min}$	2500	V
Operating junction and storage temperature range	T_J, T_{Stg}		-55 to +175	°C

ELECTRICAL SPECIFICATIONS ($T_J = 25\text{ °C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V_{BR}	$I_R = 100\text{ }\mu\text{A}$	1200	-	-	V
Forward voltage	V_{FM}	$I_F = 30\text{ A}$	-	2.91	3.57	
		$I_F = 30\text{ A}, T_J = 150\text{ °C}$	-	2.19	-	
Reverse leakage current	I_{RM}	$V_R = 1200\text{ V}$	-	0.5	60	μA
		$T_J = 125\text{ °C}, V_R = 1200\text{ V}$	-	60	-	
		$T_J = 150\text{ °C}, V_R = 1200\text{ V}$	-	214	-	

**DYNAMIC RECOVERY CHARACTERISTICS** ($T_J = 25\text{ }^{\circ}\text{C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t_{rr}	$T_J = 25\text{ }^{\circ}\text{C}$	-	41	-	ns
		$T_J = 125\text{ }^{\circ}\text{C}$	-	68	-	
Peak recovery current	I_{RRM}	$T_J = 25\text{ }^{\circ}\text{C}$	-	19	-	A
		$T_J = 125\text{ }^{\circ}\text{C}$	-	32	-	
Reverse recovery charge	Q_{rr}	$T_J = 25\text{ }^{\circ}\text{C}$	-	0.8	-	μC
		$T_J = 125\text{ }^{\circ}\text{C}$	-	2.3	-	
Junction capacitance	C_T	$V_R = 1200\text{ V}$, $f = 1\text{ MHz}$	-	13.4	-	pF

THERMAL - MECHANICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance junction to case, per diode	R_{thJC}		-	-	1.1	$^{\circ}\text{C/W}$
Thermal resistance junction to case, per module			-	-	0.55	
Thermal resistance case to heatsink, per module	R_{thCS}	Flat, greased surface	-	0.05	-	
Weight			-	30	-	g
Mounting torque		Torque per diode	-	-	1.1 (9.7)	Nm (lbf.in)
		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style			SOT-227			

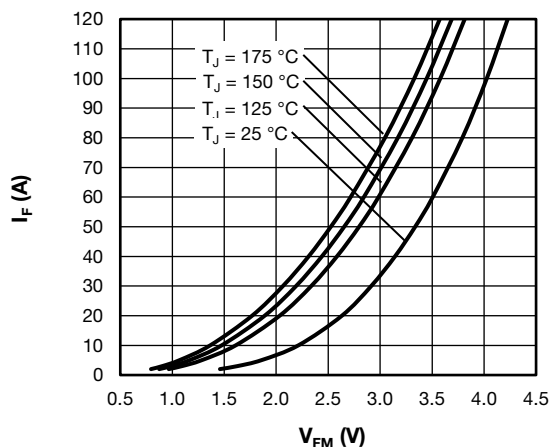


Fig. 1 - Typical Forward Voltage Drop Characteristics

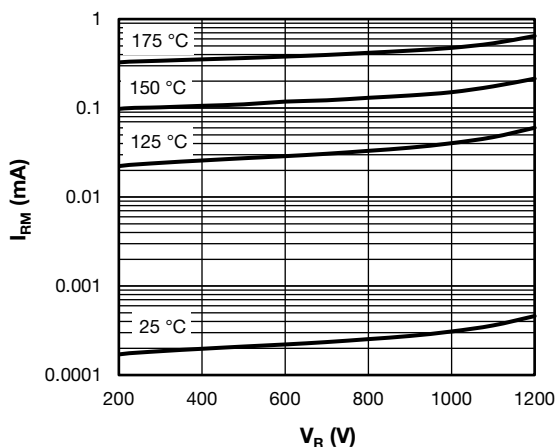


Fig. 2 - Typical Values of Reverse Current

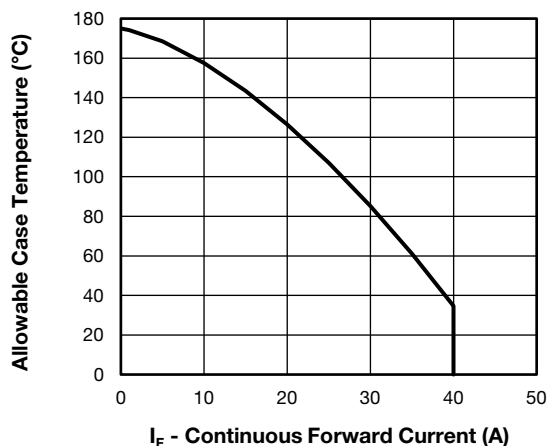


Fig. 3 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Diode)

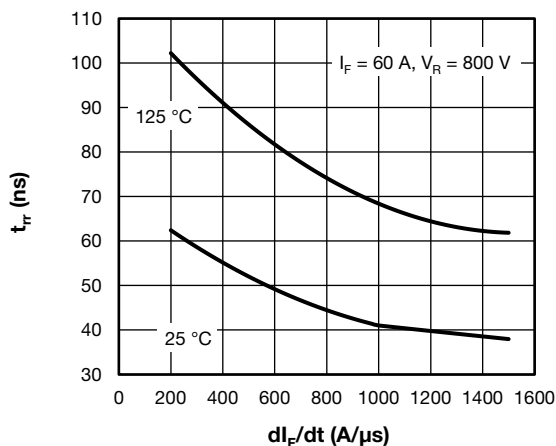


Fig. 6 - Diode Reverse Recovery Time vs. dI_F/dt

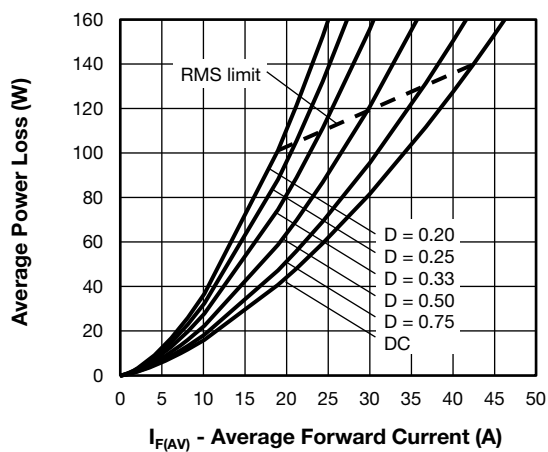


Fig. 4 - Average Power Loss vs. Average Forward Current

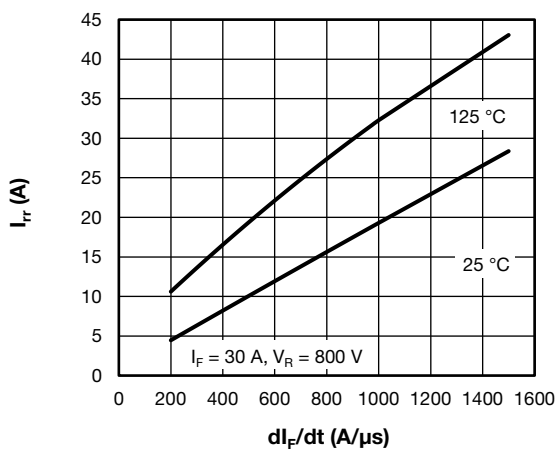


Fig. 7 - Diode Reverse Recovery Current vs. dI_F/dt

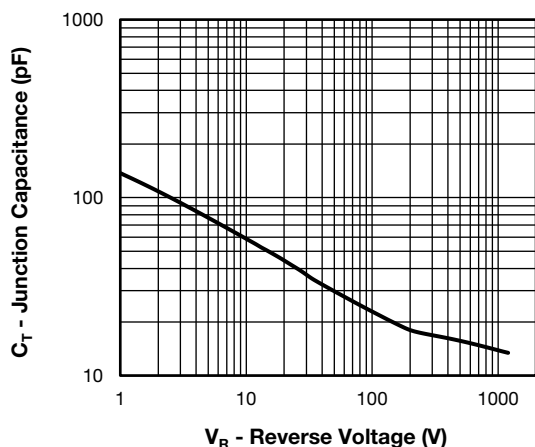


Fig. 5 - Junction Capacitance vs. Reverse Voltage

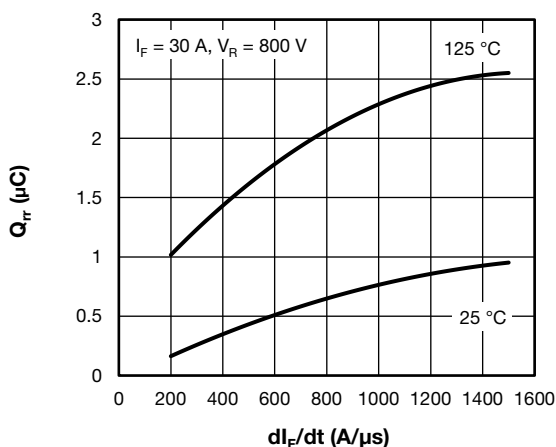


Fig. 8 - Diode Reverse Recovery Charge vs. dI_F/dt

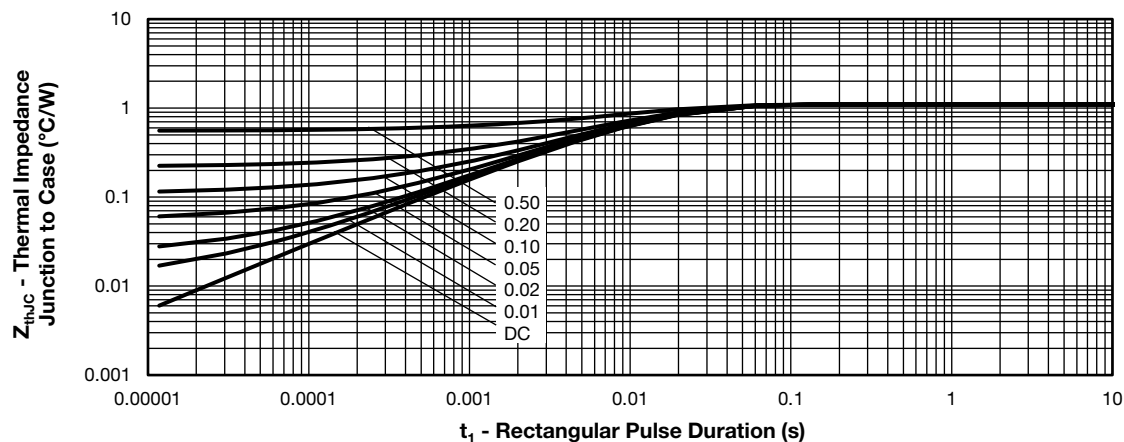


Fig. 9 - Maximum Thermal Impedance Junction to Case

ORDERING INFORMATION TABLE

Device code	VS-	U5F	X	60	F	A	120
	1	2	3	4	5	6	7

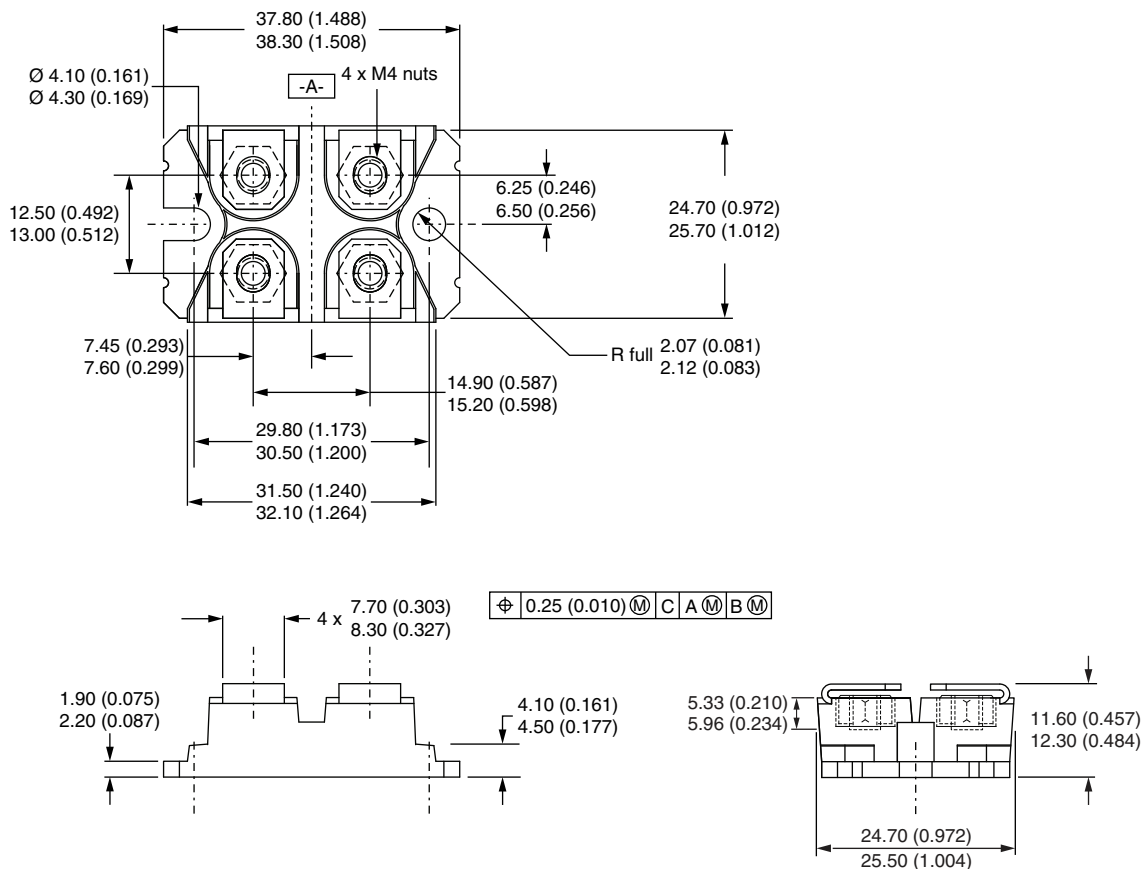
- 1 - Vishay Semiconductors product
- 2 - U5F = Gen 5 FRED Pt® family
- 3 - X = Hyperfast FRED Pt® diode
- 4 - Current rating per module (60 = 60 A)
- 5 - F = circuit configuration (two separate diodes, parallel pin-out)
- 6 - Package indicator (SOT-227 standard insulated base)
- 7 - Voltage rating (120 = 1200 V)

Circuit Configuration		
Circuit	Circuit Configuration Code	Circuit Drawing
Two separate diodes, parallel pin-out	F	The circuit drawing for configuration F consists of two parts. On the left is a schematic diagram showing two diodes connected in parallel. The top diode has its anode connected to pin 4 and its cathode to pin 3. The bottom diode has its anode connected to pin 1 and its cathode to pin 2. On the right is a physical component drawing of a dual in-line package (DIP) with four pins. The pins are numbered 1, 2, 3, and 4 from left to right. The component has two hexagonal mounting holes in the center. The text 'Lead Assignment' is written above the component drawing.

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95423
Packaging information	www.vishay.com/doc?95425

SOT-227 Generation 2

DIMENSIONS in millimeters (inches)



Note

- Controlling dimension: millimeter



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