PCN-301479

Product Change Notice

Issue Date: 07-November-2025

Change Description:

Enhanced feature changes for Multi-turn (MT) reset commands.

Parts Affected:

Broadcom Part Number	Product Description
AS33-M42M-B53/B54	18 bits ST + 24 bits MT, 5V BiSS-C, M3 or M4 Mounting
AS33-M42M-K53/K54	18 bits ST + 24 bits MT, 5V RS485, M3 or M4 Mounting
AS33-M42M-S53/S54	18 bits ST + 24 bits MT, 5V SSI2W, M3 or M4 Mounting
AS33-M42M-B73/B74	18 bits ST + 24 bits MT, 7V-12V BiSS-C, M3 or M4 Mounting
AS33-M42M-K73/K74	18 bits ST + 24 bits MT, 7V-12V RS485, M3 or M4 Mounting
AS33-M42M-S73/S74	18 bits ST + 24 bits MT, 7V-12V SSI2W, M3 or M4 Mounting
AS20-M42M-B11	20mm Kits 42 bits MT 5V 4mm BiSS-C
AS20-M42M-S11	20mm Kits 42 bits MT 5V 4mm SSI 2-Wire
AS20-M42M-P11	20mm Kits 42 bits MT 5V 4mm SPI 4-Wire
AS20-M42M-K11	20mm Kits 42 bits MT 5V 4mm RS485
AEAT-9988M-D30	High Accuracy Magnetic Encoder, 16 bits MT & 23 bits ST, SSI SPI RS-485
AEAT-9988M-102	High Accuracy Magnetic Encoder, 16 bits MT & 23 bits ST, SSI SPI RS-485
AEAT-9988M-104	High Accuracy Magnetic Encoder, 16 bits MT & 23 bits ST, SSI SPI RS-485
AEAT-9988MB-D30	High Accuracy Magnetic Encoder, 16 bits MT & 23 bits ST, SSI SPI RS-485 BiSS-C
AEAT-9988MB-102	High Accuracy Magnetic Encoder, 16 bits MT & 23 bits ST, SSI SPI RS-485 BiSS-C
AEAT-9988MB-104	High Accuracy Magnetic Encoder, 16 bits MT & 23 bits ST, SSI SPI RS-485
HEDS-9988MEVB	AEAT-9988M SSI SPI RS485 Evaluation Board
HEDS-9988MBEVB	AEAT-9988MB SSI SPI RS485 BiSS-C Evaluation Board

Description and Extent of Change:

Table 2: Change Descriptions

Before	Enhancement
Upon multi-turn (MT) reset command, there is a dependency of the new MT position value if the single-turn (ST) zero offset registers are non-zero. Depending on the ST angle position, the MT reset may return -1 (i.e. FFFFFF if MT resolution is 24-bit, depending on the resolution) or zero. The exact position is depending on the offset values in the ST zero offset register.	Upon MT reset command, no dependency of the new MT position value on the ST zero offset register values.

Reasons for Change:

This is a product feature enhancement for improvement in multi-turn zero reset handling.

Effect of Change on Fit, Form, Function, Quality, or Reliability:

The changes have no impact on product form, fit, function and performance at the product level.

Effective Date of Change:

Product shipment using this change effectively on or around November 2025.

Please contact your Broadcom field sales engineer or Contact Center for any questions or support requirements. Please acknowledge the receipt of the notice within 30 days of delivery. Lack of acknowledgement within 30 days constitutes acceptance of the change per JEDEC J-STD-046.